

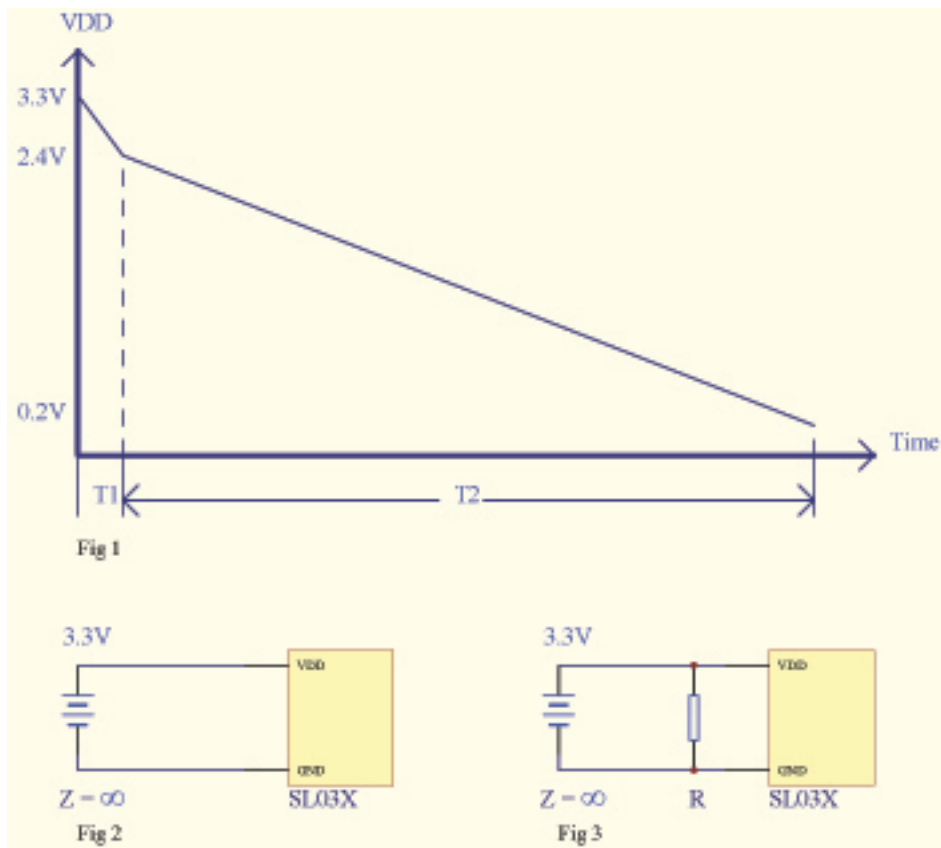
# VDD Power Cycling

## Relevant Devices

This application note applies to the following devices  
SL030, SL031, SL032

## Description

To generate a proper Power-On-Reset for SL030/SL031/SL032, VDD must have dropped below 0.2V before being powered back up. Power-cycling without VDD having dropped below 0.2V may result in incorrect Program Counter values.



SL030/SL031/SL032 will can't run correctly when power-cycling appearing during T2. In extreme conditions, there are no any discharge components in the circle as Fig 1. T1 = 20us, T2 = 2.2s

To reduce T2, a discharge resistor is optional as Fig 2. At this time,  $T2 \approx 3 * 10^{-7} * R$